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Spartan-3AN FPGA Family: Introduction and Ordering Information DS557 (v4.2) June 12, 2014
www.xilinx.com Product Specification 3 Architectural Overview

Spartan-3AN FPGA Family Data Sheet (DS557) - Xilinx

A field-programmable gate array (FPGA) is an integrated circuit designed to be configured by a customer or a designer after manufacturing – hence "field-programmable". ...

Field-programmable gate array - Wikipedia

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Intel® Quartus® Prime Software - Intel® FPGA Development

1. Design Example Quick Start Guide for External Memory Interfaces Intel® Cyclone® 10 FPGA IP A new interface and more automated design example flow is available for Intel

256 10 FPGA IP Design Example User Guide - intel.com

Design Software: Libero SoC Design Software integrates industry leading synthesis, debug and DSP support from Synopsys, and simulation from Mentor Graphics with power analysis, timing analysis and push button design flow.

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ECP5 / ECP5-5G - Lattice Semiconductor

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iCE40 Ultra / UltraLite - Lattice Semiconductor

Spartan-II FPGA Family: Introduction and Ordering Information DS001-1 (v2.8) June 13, 2008
www.xilinx.com Module 1 of 4 Product Specification 3 R General Overview

Xilinx DS001 Spartan-II FPGA Family data sheet

System Design Journal. Help and solutions for tomorrow's design. by Ron Wilson, Editor-in-Chief

Documentation Index - Intel FPGA and SoC

Intel licenses Mentor Graphics* ModelSim-Intel FPGA simulation software to customers with the Intel Quartus Prime design software. Both ModelSim-Intel FPGA Edition and ModelSim-Intel FPGA Starter Edition are available for Lite Edition, Standard Edition and Pro Edition software users.

Simulation/Model - ModelSim* - Intel FPGA - Altera

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Free Electronics resources: schematics, tutorials

MIL-STD-188 is a series of U.S. military standards relating to telecommunications

MIL-STD-188 - Wikipedia

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